

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	1015	(first second third) adj print\$2 adj circuit adj board same (plurality multiple array linear\$2 align\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 11:29
L4	366	(first second third) adj print\$2 adj circuit adj board same (array linear\$2 align\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 11:28
L5	649	L3 not L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 11:28
L8	11290	print\$2 adj circuit adj board same spring	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 12:34
L9	2600	print\$2 adj circuit adj board near5 spring	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 12:34
L10	2449	printed adj circuit adj board near5 spring	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 13:44
L11	3	("6439908").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 13:45
L13	760	(first second third) adj print\$2 adj circuit adj board same end	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/03 14:54

S2	2	("20050157043").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/02 14:15
S6	46	((print\$2 adj circuit adj board) PCB) same (linear\$2 align\$2) same spring same power same data	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 15:24
S7	29484	((print\$2 adj circuit adj board) PCB (circuit adj board) (print\$2 adj board)) same (linear\$2 align\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 15:28
S8	54	((print\$2 adj circuit adj board) PCB (circuit adj board) (print\$2 adj board)) same (linear\$2 align\$2) same spring same power same data	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 15:25
S9	8	S8 not S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 15:25
S10	28624	((print\$2 adj circuit adj board) PCB (circuit adj board)) same (linear\$2 align\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 15:28
S11	17285	((print\$2 adj circuit adj board) PCB) same (linear\$2 align\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 15:28
S12	5099	((print\$2 adj circuit adj board) PCB) near5 (linear\$2 align\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 15:30
S13	4048	((print\$2 adj circuit adj board) PCB) near5 (linear\$2 align\$2)	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 15:32

S14	4385	((print\$2 adj circuit adj board) PCB) near5 (array)	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 15:32
S15	202	((print\$2 adj circuit adj board) PCB) near5 (array) same linear\$2	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/02 15:54
S16	140648	("361").CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/02 15:54
S17	2024	S11 and S16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/02 15:54
S18	657	S12 and S16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/02 15:56
S19	509	S14 and S16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/02 15:59
S20	2933	(first second third) adj print\$2 adj circuit adj board	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 14:53
S22	63819	("347").CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/02 16:00
S23	583	(S16 S22) and S20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/02 16:01

S24	365	(first second third) adj print\$2 adj circuit adj board same (array linear\$2 align\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/03 11:27
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